

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Manjunath Haritsa, Manishkumar Ankola, Ralf M. Schmitt, Anup Sharma, Stephan Hoerold and David M. Murata

Assignee: Sun Microsystems, Inc.

Title: CLOCK SKEW VERIFICATION METHODOLOGY FOR GRID-BASED DESIGN

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Monterey, CA  
June 28, 2005

Mail Stop Issue Fee  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION OF REPLACEMENT SHEETS OF DRAWINGS

Dear Sir:

The attached eight (8) replacement sheets of drawings correct minor informalities and generally conform to USPTO drawing guidelines for Fig(s). 1, 2, 3, 4, 5, 6, 7 and 8.

Sheet one, which includes Fig. 1, replaces the original sheet one including Fig. 1.

Sheet two, which includes Fig. 2, replaces the original sheet two including Fig. 2.

Sheet three, which includes Fig. 3, replaces the original sheet three including Fig. 3.

Sheet four, which includes Fig. 4, replaces the original sheet four including Fig. 4.

Sheet five, which includes Fig. 5, replaces the original sheet five including Fig. 5.

Sheet six, which includes Fig. 6, replaces the original sheet six including Fig. 6.

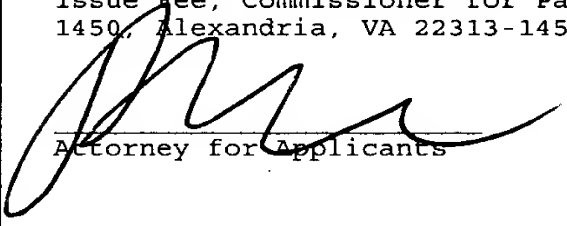
Sheet seven, which includes Fig. 7, replaces the original sheet seven including Fig. 7.

Sheet eight, which includes Fig. 8, replaces the original sheet eight including Fig. 8.

If there are any questions concerning these replacement sheets, please call the undersigned at the number given.

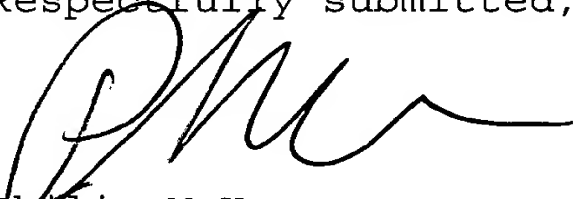
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, June 28, 2005.

  
Attorney for Applicants

June 28, 2005  
Date of Signature

Respectfully submitted,

  
Philip McKay  
Attorney for Applicants  
Registration No. 38,966  
Tel.: (831) 655-0880